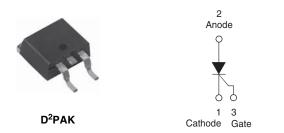
Vishay Semiconductors

# **Thyristor Surface Mount, Phase Control SCR, 8 A**



PRODUCT SUMMARY								
Package	TO-263AB (D <sup>2</sup> PAK)							
Diode variation	Single SCR							
I <sub>T(AV)</sub>	8 A							
V <sub>DRM</sub> /V <sub>RRM</sub>	800 V							
V <sub>TM</sub>	1.2 V							
I <sub>GT</sub>	15 mA							
TJ	- 40 to 125 °C							

#### **FEATURES**

- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Designed and qualified according JEDEC-JESD47
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### APPLICATIONS

- · Input rectification and crow-bar (soft start)
- Vishay input diodes, switches and output rectifiers which are available in identical package outlines

## DESCRIPTION

The VS-12TTS08SPbF High Voltage Series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature.

OUTPUT CURRENT IN TYPICAL APPLICATIONS							
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS				
Capacitive input filter $T_A = 55 \text{ °C}$ , $T_J = 125 \text{ °C}$ , common heatsink of 1 °C/W	13.5	17	A				

MAJOR RATINGS AND CHARACTERISTICS								
PARAMETER	TEST CONDITIONS	VALUES	UNITS					
I <sub>T(AV)</sub>	Sinusoidal waveform	8	A					
I <sub>T(RMS)</sub>		12.5	A					
V <sub>RRM</sub> /V <sub>DRM</sub>		800	V					
I <sub>TSM</sub>		110	А					
V <sub>T</sub>	8 A, T <sub>J</sub> = 25 °C	1.2	V					
dV/dt		150	V/µs					
dl/dt		100	A/µs					
TJ	Range	- 40 to 125	°C					

VOLTAGE RATINGS			
PART NUMBER	V <sub>RRM</sub> , MAXIMUM PEAK REVERSE VOLTAGE V	V <sub>DRM</sub> , MAXIMUM PEAK DIRECT VOLTAGE V	I <sub>RRM</sub> /I <sub>DRM</sub> AT 125 °C mA
VS-12TTS08SPbF	800	800	1.0

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HALOGEN

FREE



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## VS-12TTS08SPbF Series

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PARAMETER	SYMBOL		TEST CONDITIONS	VALUES	UNITS
Maximum average on-state current	I <sub>T(AV)</sub>	т 100.00 г		8	
Maximum RMS on-state current	I <sub>T(RMS)</sub>	$T_{\rm C} = 108$ °C,	180° conduction, half sine wave	12.5	•
Maximum peak one-cycle		10 ms sine pu	llse, rated $V_{RRM}$ applied, $T_J = 125 \text{ °C}$	95	A
non-repetitive surge current	I <sub>TSM</sub>	10 ms sine pulse, no voltage reapplied, $T_J$ = 125 °C		110	
Maximum I <sup>2</sup> t for fusing	l <sup>2</sup> t	10 ms sine pu	llse, rated $V_{RRM}$ applied, $T_J = 125 \text{ °C}$	45	A <sup>2</sup> s
Maximum 1-t for fusing	1-1	10 ms sine pu	64	A-S	
Maximum I <sup>2</sup> $\sqrt{t}$ for fusing	l²√t	t = 0.1 ms to 7	640	A²√s	
Maximum on-state voltage drop	V <sub>TM</sub>	8 A, T <sub>J</sub> = 25 °	1.2	V	
On-state slope resistance	r <sub>t</sub>			16.2	mΩ
Threshold voltage	V <sub>T(TO)</sub>	T <sub>J</sub> = 125 °C		0.87	V
Maximum reverse and direct lockage current	1 /1	T <sub>J</sub> = 25 °C		0.05	
Maximum reverse and direct leakage current	I <sub>RM</sub> /I <sub>DM</sub>	T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>RRM</sub> /V <sub>DRM</sub>	1.0	
Typical holding current	Ι <sub>Η</sub>	Anode supply T <sub>J</sub> = 25 °C	30	mA	
Maximum latching current	١ <sub>L</sub>	Anode supply	50		
Maximum rate of rise of off-state voltage	dV/dt	$T_J = T_J max., I$	linear to 80 %, $V_{DRM} = R_g - k = Open$	150	V/µs
Maximum rate of rise of turned-on current	dl/dt			100	A/µs

TRIGGERING								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum peak gate power	P <sub>GM</sub>		8.0	W				
Maximum average gate power	P <sub>G(AV)</sub>		2.0	vv				
Maximum peak positive gate current	+ I <sub>GM</sub>		1.5	А				
Maximum peak negative gate voltage	- V <sub>GM</sub>		10	V				
	I <sub>GT</sub>	Anode supply = 6 V, resistive load, $T_J$ = - 65 °C	20	mA				
Maximum required DC gate current to trigger		Anode supply = 6 V, resistive load, $T_J = 25 \ ^{\circ}C$	15					
		Anode supply = 6 V, resistive load, $T_J$ = 125 °C	10					
		Anode supply = 6 V, resistive load, $T_J$ = - 65 °C	1.2					
Maximum required DC gate voltage to trigger	V <sub>GT</sub>	Anode supply = 6 V, resistive load, $T_J = 25 \text{ °C}$	1					
		Anode supply = 6 V, resistive load, $T_J$ = 125 °C	0.7	V				
Maximum DC gate voltage not to trigger	$V_{GD}$	T 125 °C V Batad value	0.2					
Maximum DC gate current not to trigger	I <sub>GD</sub>	T <sub>J</sub> = 125 °C, V <sub>DRM</sub> = Rated value	0.1	mA				

SWITCHING									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Typical turn-on time	t <sub>gt</sub>	T <sub>J</sub> = 25 °C	0.8						
Typical reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 125 °C	3	μs					
Typical turn-off time	tq	ij= 125 C	100						

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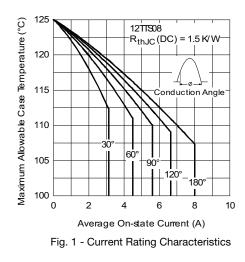
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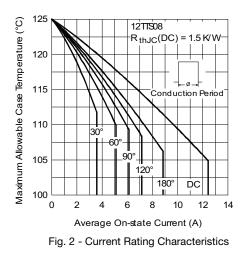
## VS-12TTS08SPbF Series

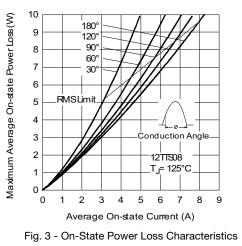


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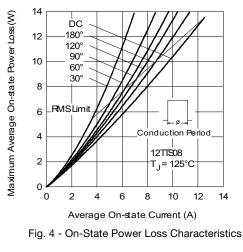
THERMAL AND MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and sto temperature range	rage	T <sub>J</sub> , T <sub>Stg</sub>		- 40 to 125	°C			
Maximum thermal resistan junction to case	· · · · · · · · · · · · · · · · · · ·		DC operation	1.5				
Maximum thermal resistance, junction to ambient		R <sub>thJA</sub>		62	°C/W			
Typical thermal resistance case to heatsink	,	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.5				
Approvimete weight				2	g			
Approximate weight				0.07	oz.			
Mounting torgue	minimum			6 (5)	kgf ⋅ cm			
Mounting torque —	maximum			12 (10)	(lbf · in)			
Marking device			Case style D <sup>2</sup> PAK (SMD-220)	12TT	S08S			











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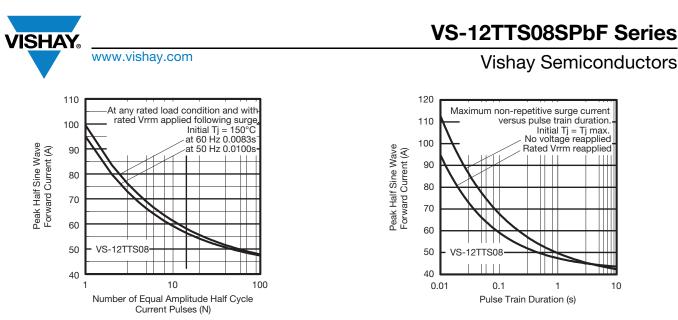


Fig. 5 - Maximum Non-Repetitive Surge Current

Fig. 6 - Maximum Non-Repetitive Surge Current

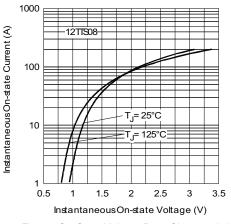


Fig. 7 - On-State Voltage Drop Characteristics

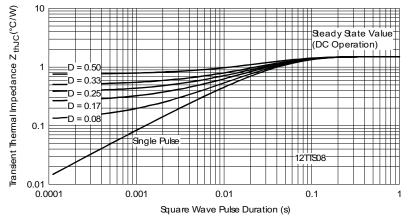
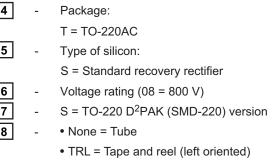


Fig. 8 - Thermal Impedance  $Z_{thJC}$  Characteristics



• TRR = Tape and reel (right oriented)

PbF = Lead (Pb)-free

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ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-12TTS08SPbF	50	1000	Antistatic plastic tubes						
VS-12TTS08STRRPbF	800	800	13" diameter reel						
VS-12TTS08STRLPbF	800	800	13" diameter reel						

LINKS TO RELATED DOCUMENTS							
Dimensions	www.vishay.com/doc?95046						
Part marking information	www.vishay.com/doc?95054						
Packaging information	www.vishay.com/doc?95032						

# VS-12TTS08SPbF Series

## Vishay Semiconductors



**ORDERING INFORMATION TABLE** 

Device code	VS-	12	т	т	S	08	S	TRL	PbF
		2	3	4	5	6	7	8	9
	1 - 2 -			niconduo ng (12.5	•	oduct			
	3 -	Circ	uit conf	iguratior hyristor					
	4 -		kage: TO-220	AC					
	5 -		e of silio Standa	con: rd recov	ery rect	ifier			
	6 - 7 -		-	ng (08 = ) D <sup>2</sup> PAK	-		rsion		
	8 -		one = Tu RL = Tap	ube be and re	eel (left	orienteo	d)		

## **Outline Dimensions**

**Vishay Semiconductors** 

D<sup>2</sup>PAK



Conforms to JEDEC outline D<sup>2</sup>PAK (SMD-220) в Pad layout (2)(3)A 11.00 MIN.-(E) F (0.43)ŧ (3) L1 4 ( |(0.38)<sup>MIN.</sup> (D1) (3) Detail A D 17.90 (0.70) Н 15.00 (0.625) (2) З 0.15)<sup>0.01</sup> Ľ L2 Ĥ ţ В В 2.32 MIN. (0.08) 2.64 (0.103) 2.41 (0.096) (3)Ċ 2 x b2 С View A - A 2 x h // ± 0.004 M B ⊕ 0.010 M A M B Base Plating (4) Metal 2 x e Н b1, b3 Gauge plane c1 (4) (c) В 0° to 8° ŧ. Seating Lead assignments plane L3 A1 Lead tip (b, b2) Diodes Section B - B and C - C 1. - Anode (two die)/open (one die) Scale: None 2., 4. - Cathode Detail "A" 3. - Anode

Rotated 90 °CW Scale: 8:1

SYMBOL	MILLIMETERS		INCHES		HES NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
с	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3 0.25 BSC 0.010 BSC		BSC			
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

#### Notes

 $^{(1)}\,$  Dimensioning and tolerancing per ASME Y14.5 M-1994  $\,$ 

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

<sup>(5)</sup> Datum A and B to be determined at datum plane H

<sup>(6)</sup> Controlling dimension: inch

<sup>(7)</sup> Outline conforms to JEDEC outline TO-263AB

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#### **DIMENSIONS** in millimeters and inches



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