



30V COMPLEMENTARY ENHANCEMENT MODE MOSFET H-BRIDGE

Product Summary

Device	V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = 25°C
N Channal	201/	$25m\Omega$ @ $V_{GS} = 10V$	6.0
N-Channel	30V	$40m\Omega$ @ $V_{GS} = 4.5V$	4.6
D. Ohannal 2007		$50m\Omega$ @ $V_{GS} = -10V$	-4.2
P-Channel	-30V	$80m\Omega$ @ $V_{GS} = -4.5V$	-3.2

Description

This new generation complementary MOSFET H-Bridge features low on-resistance achievable with low gate drive.

Applications

- DC Motor control
- DC-AC Inverters

Features

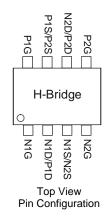
- 2 x N + 2 x P channels in a SOIC package
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

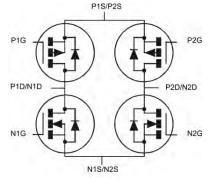
Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.008 grams (approximate)









Internal Schematic

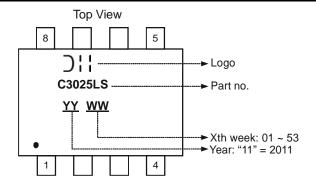
Ordering Information (Note 4)

Part Number	Case	Packaging
DMHC3025LSD-13	SO-8	2500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and 1000ppm antimony compounds.
 4. For packaging details, go to our website at http://www.diodes.com.

Marking Information





Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Total Power Dissipation (Note 5)		P_{D}	1.5	W	
Thermal Begintenes, Junction to Ambient (Note 5)	Steady State	Б	83		
Thermal Resistance, Junction to Ambient (Note 5) t < 10s		$R_{\theta JA}$	50	°C/W	
Thermal Resistance, Junction to Case		$R_{\theta JC}$	14.5		
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to 150	°C	

Maximum Ratings N-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			V_{GSS}	±20	V
		$T_A = +25$ °C $T_A = +70$ °C	I _D	6.0 4.8	А
Continuous Drain Current (Note 5) V _{GS} = 10V	t < 10s	$T_A = +25$ °C $T_A = +70$ °C	I _D	7.8 6.1	А
Continuous Drain Current (Note 5) $V_{GS} = 4.5V$		$T_A = +25$ °C $T_A = +70$ °C	I _D	4.6 3.6	А
		$T_A = +25$ °C $T_A = +70$ °C	I _D	6.1 4.8	А
Maximum Continuous Body Diode Forward Current (Note 5)			I _S	2.5	Α
Pulsed Drain Current (10μs pulse, duty cycle = 1%)			I _{DM}	60	Α

Maximum Ratings P-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Drain-Source Voltage			V_{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±20	V
State		$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-4.2 -3.3	А
Continuous Drain Current (Note 5) V _{GS} = -10V	t < 10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-5.4 -4.3	А
Continuous Drain Current (Note 5) $V_{GS} = -4.5V$ $t < $		$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-3.2 -2.5	А
		$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-4.3 -3.3	А
Maximum Continuous Body Diode Forward Current (Note 5)			Is	-2.5	Α
Pulsed Drain Current (10μs pulse, duty cycle = 1%)			I _{DM}	-30	Α

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.



Electrical Characteristics N-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	-	-	0.5	μΑ	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	-	-	±1	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V _{GS(th)}	1.0	-	2.0	٧	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance		-	19	25	mΩ	$V_{GS} = 10V, I_D = 5A$
Static Dialif-Source Off-Resistance	R _{DS (ON)}	-	26	40	11122	$V_{GS} = 4.5V, I_D = 4A$
Forward Transfer Admittance	Y _{fs}	-	4	-	S	$V_{DS} = 5V$, $I_D = 5A$
Diode Forward Voltage	V_{SD}	-	0.70	1.2	V	$V_{GS} = 0V, I_{S} = 1.7A$
DYNAMIC CHARACTERISTICS (Note 7)						
Input Capacitance	C _{iss}	-	590	-		V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	Coss	-	122	-	pF	
Reverse Transfer Capacitance	Crss	-	58	-		
Gate resistance	R_g	-	1.5	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V _{GS} = 4.5V)	Q_{g}	-	5.4	-		
Total Gate Charge (V _{GS} = 10V)	Qg	-	11.7	-	nC	V _{DS} = 15V. I _D = 7.8A
Gate-Source Charge	Q_{gs}	-	1.8	-	IIC	V _{DS} = 15V, I _D = 7.8A
Gate-Drain Charge	Q _{gd}	-	2.1	-		
Turn-On Delay Time	t _{D(on)}	-	11.2	-		$\begin{split} V_{DD} &= 15 V, \ V_{GS} = 4.5 V, \\ R_L &= 2.4 \Omega, \ R_G = 1 \Omega, \end{split}$
Turn-On Rise Time	t _r	-	15.0	-		
Turn-Off Delay Time	t _{D(off)}	-	17.5	-	ns	
Turn-Off Fall Time	t _f	-	8.7	-		
Reverse Recovery Time	t _{rr}	-	18.3	-	ns	1 400 1:/14 5000///
Reverse Recovery Charge	Q _{rr}	-	12.0	-	nC	I _F = 12A, di/dt = 500A/μs

Electrical Characteristics P-CHANNEL (@T_A = +25°C, unless otherwise specified.)

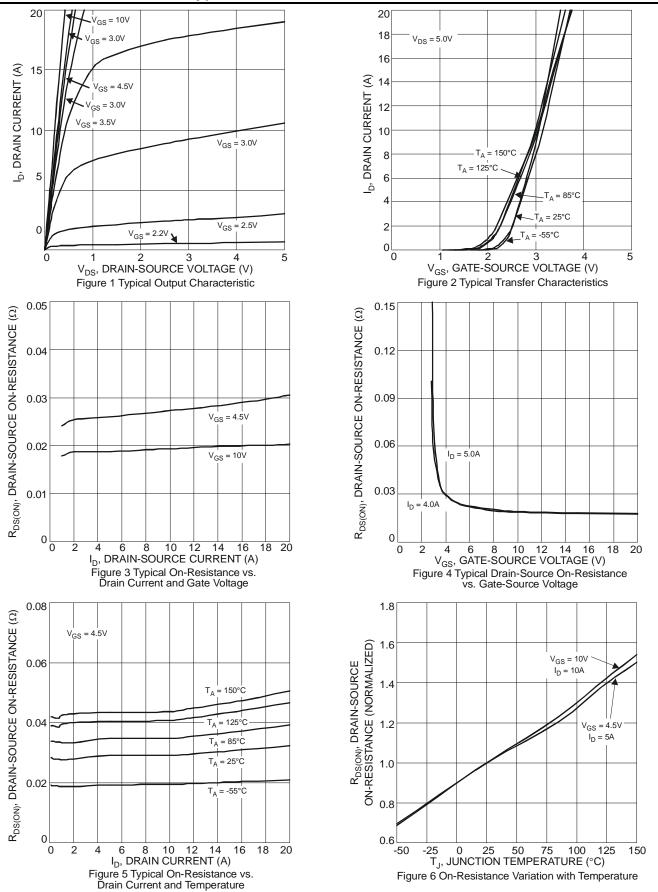
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV _{DSS}	-30	-	-	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	-	1	-0.5	μΑ	$V_{DS} = -30V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	-	-	±1	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)	a .					_
Gate Threshold Voltage	$V_{GS(th)}$	-1.0	-	-2.0	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$
Static Drain-Source On-Resistance	P== (=1)	-	43	50	mΩ	$V_{GS} = -10V, I_D = -5A$
Static Drain-Source On-Nesistance	R _{DS (ON)}	-	68	80	111 22	$V_{GS} = -4.5V, I_D = -4A$
Forward Transfer Admittance	Y _{fs}	-	3.5	-	S	$V_{DS} = -15V, I_{D} = -5A$
Diode Forward Voltage	V_{SD}	-	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -1.7A$
DYNAMIC CHARACTERISTICS (Note 7)						
Input Capacitance	C _{iss}	-	631	-	pF	151/1/
Output Capacitance	Coss	-	137	-	pF	$V_{DS} = -15V, V_{GS} = 0V,$ -f = 1.0MHz
Reverse Transfer Capacitance	Crss	-	70	-	pF	1 = 1.0WH12
Gate resistance	R_{g}	-	10.8	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V _{GS} = 4.5V)	Q_{g}	-	5.5	-	nC	
Total Gate Charge (V _{GS} = 10V)	Q_g	-	11.4	-	nC	$V_{DS} = -15V, I_{D} = -6A$
Gate-Source Charge	Q _{gs}	-	1.8	-	nC	V _{DS} = -15V, I _D = -6A
Gate-Drain Charge	Q_{gd}	-	2.4	-	nC	
Turn-On Delay Time	t _{D(on)}	-	7.5	-	ns	
Turn-On Rise Time	t _r	-	4.9	-	ns	$V_{DD} = -15V, V_{GS} = -10V,$
Turn-Off Delay Time	t _{D(off)}	-	28.2	-	ns	$R_G = 6\Omega$, $I_D = -1A$
Turn-Off Fall Time	t _f	-	13.5	-	ns	
Reverse Recovery Time	t _{rr}	-	15.1	-	ns	1 42A di/dt 500A/uc
Reverse Recovery Charge	Q _{rr}		15.3	-	nC	$I_F = 12A$, di/dt = 500A/ μ s

Notes: 6. Short duration pulse test used to minimize self-heating effect.

^{7.} Guaranteed by design. Not subject to product testing.



Typical Characteristics - N-CHANNEL





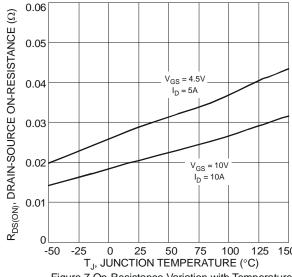
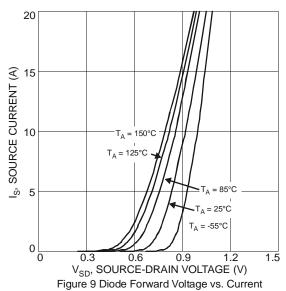
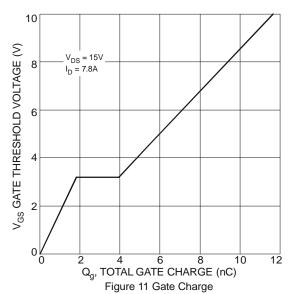


Figure 7 On-Resistance Variation with Temperature





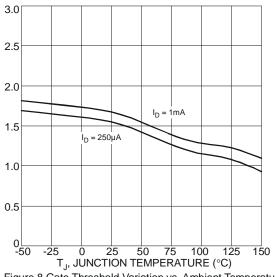
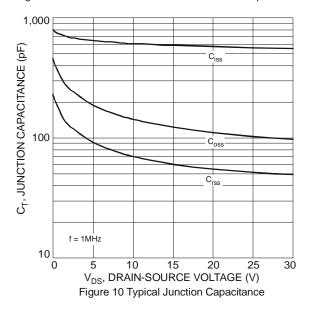


Figure 8 Gate Threshold Variation vs. Ambient Temperature





Typical Characteristics - P-CHANNEL

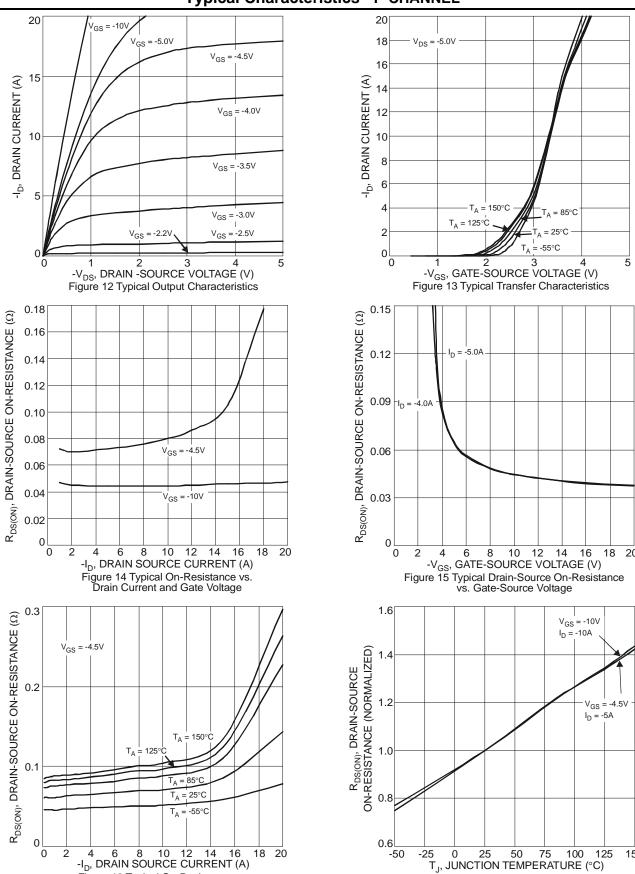
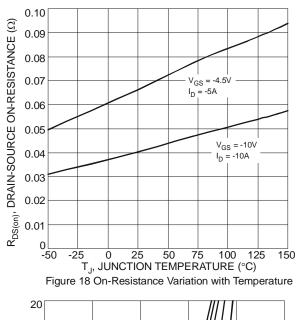
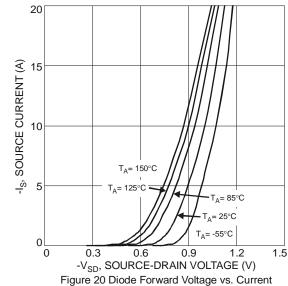


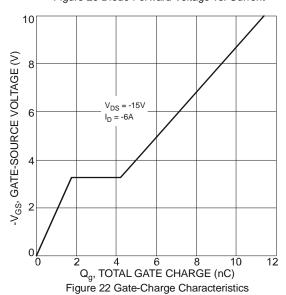
Figure 16 Typical On-Resistance vs.
Drain Current and Temperature

Figure 17 On-Resistance Variation with Temperature









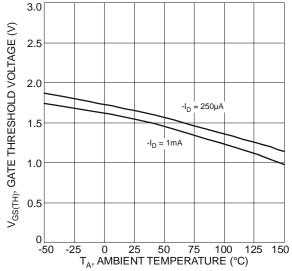
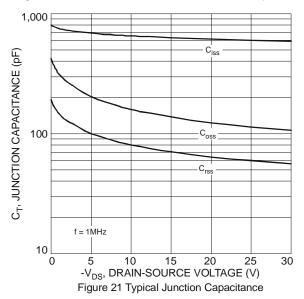


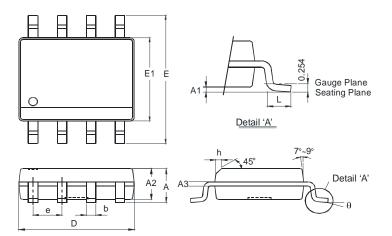
Figure 19 Gate Threshold Variation vs. Ambient Temperature





Package Outline Dimensions

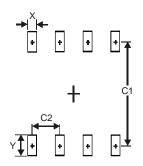
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SO-8					
Dim	Min	Max				
Α	-	1.75				
A1	0.10	0.20				
A2	1.30	1.50				
А3	0.15	0.25				
b	0.3	0.5				
D	4.85	4.95				
Е	5.90	6.10				
E1	3.85 3.95					
е	1.27	Тур				
h	-	0.35				
L	0.62	0.82				
θ	0° 8°					
All Di	All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Х	0.60
Υ	1.55
C1	5.4
C2	1.27



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